

~~IN THE SPECIFICATION~~

~~Please replace the following paragraphs:~~

~~Page 1, lines 9 to 14.~~  
IDC-A1,AMD

U.S. Serial Number 09/567,941, filed May 10, 2000, now

This application claims priority to and is a continuation of co-owned ~~co-pending~~ United States Patent Application Serial No. ~~09/567,941~~, filed May 10, 2000, by No. 6,691,253, issued February 10, 2004, to Gillenwater et al., entitled SYSTEM AND METHOD FOR SEQUENCING AND PERFORMING VERY HIGH SPEED SOFTWARE DOWNLOADS CONCURRENT WITH SYSTEM TESTING IN AN AUTOMATED PRODUCTION ENVIRONMENT, which is incorporated herein by reference in its entirety.

~~Page <sup>1</sup>8, lines 21 to 25.~~  
IDC-A2,AMD

This application relates to ~~co-pending~~ United States Patent Application Serial No. ~~09/277,062~~, filed on March 26, 1999 No. 6,591,418, issued July 8, 2003, entitled FACTORY SOFTWARE MANAGEMENT SYSTEM naming Joe Bryan, Steve Romohr, Jon Boede, Gaston M. Barajas and Paul J. Maia as inventors. ~~The co-pending application~~ This patent is incorporated herein by reference in its entirety, and is assigned to the assignee of this invention.

~~Page 2, lines 1 to 6.~~  
IDC-A3,AMD,M

This application relates to ~~co-pending~~ United States Patent Application Serial No. ~~09/315,657~~, filed on May 20, 1999, No. 6,385,766, issued May 7, 2002, entitled

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IDC-A3,AMD,M

METHOD AND APPARATUS FOR WINDOWS-BASED INSTALLATION FOR  
INSTALLING SOFTWARE ON BUILD-TO-ORDER COMPUTER SYSTEMS naming  
Bobby G. Doran, Jr., Bill Hyden and Terry Wayne Liles as inventors. ~~The co-pending  
application~~ This patent is incorporated herein by reference in its entirety, and is  
assigned to the assignee of this invention.

~~Page 2, lines 8 to 14.~~

IDC-A4,AMD

This application relates to ~~co-pending~~ United States Patent Application Serial No.  
~~09/333,786, filed on June 15, 1999, No. 6,543,047, issued April 1, 2003,~~ entitled  
METHOD AND APPARATUS FOR TESTING CUSTOM-CONFIGURED  
SOFTWARE/HARDWARE INTEGRATION IN A COMPUTER BUILD-TO-ORDER  
MANUFACTURING PROCESS naming Thomas Vrhel, Jr., Gaston M. Barajas, Paul J.  
Maia and Todd Nix as inventors. ~~The co-pending application~~ This patent is incorporated  
herein by reference in its entirety, and is assigned to the assignee of this invention.

~~Page 6, lines 3 to <sup>8</sup>7.~~

IDC-A5,AMD

Fig. 1 is a block diagram of a software installation and testing system ~~embodying~~  
illustrating features of one embodiment.

Figs. 1A-1C respectively are process flow diagrams illustrating operation of  
various embodiments, including a prior art embodiment (Fig. 1A).

Fig. 1D is a flowchart illustrating an embodiment of the operation of the system of  
Fig. 1.

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